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Electronic Version v1.1

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Title of Invention	Method and Control Circuit for Power Factor Correction	
Application Number : Date :		
First Named Applicant; Mr. Isaac Cohen		
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Submitted By:	Elec. Sign.	Sign. Capacity
Mr. Isaac Cohen	Isaac Cohen	Inventor

Documents being submitted: Files PFC-usdecl.xml us-declaration us-declaration.dtd us-declaration.xsl PFC-usfees.xml us-fee-sheet us-fee-sheet.xsl us-fee-sheet.dtd PFC-usidst.xml us-ids us-ids.dtd us-ids.xsl us-request PFC-usrequ.xml us-request.dtd us-request.xsl PFCcontrol-trans.xml application-body us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Figure1.tif Figure2.tif Figure3.tif Figure4.tif Figure5.tif Figure6.tif Comments